



SON-2009

PATENT APPLICATION

GP 2818

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent Application of

Kazushi SATO et al

Application No. 09/735,938

Filed: December 14, 2000

For: NONVOLATILE SEMICONDUCTOR

MEMORY DEVICE AND METHODS FOR

OPERATING AND PRODUCING THE SAME

Group Art Unit: 2818

Examiner: To Be Assigned

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The applicants, through their representatives and attorneys, hereby bring to the attention of the Examiner the document(s) noted on the accompanying Form PTO-1449. A copy of each reference(s) is provided for the Examiner's consideration. This reference was at page 3 of the specification as filed.

Please take this document into account in the initial examination of this application and make its consideration of record.

Respectfully submitted,

Dated: April 19, 2001

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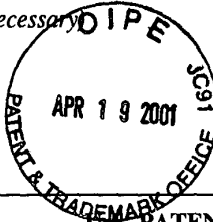
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INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				Atty. Docket No.		Serial No.	
				SON-2009		09/735,938	
				Applicant Toshio KOBAYASHI			
Filing Date December 14, 2000				Group 2818			
 U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Page, Etc.)							
			Eitan, Boaz, Paola Pavan, Ilan Bllom, Efraim Aloni, Aviv Frommer and David Finzi. <u>Can NROM, a 2-Bit, Trapping Storage NVM Cell, Give a Real Challenge to Floating Gate Cells?</u> . 1999 International Conference on Solid State Device and Materials, Tokyo, 1999, pp. 5220524.				
EXAMINER				DATE CONSIDERED			
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							